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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/405,210		09/24/1999	BRUCE D. MARCHANT	18865-32US	9239	
20350	7590	06/10/2002				
		TOWNSEND AN	EXAMINER			
TWO EMBARCADERO CENTER EIGHTH FLOOR				VU, DAVID		
SAN FRAN	CISCO, C	CA 94111-3834				
				ART UNIT	PAPER NUMBER	
				2818		
			•	DATE MAILED: 06/10/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

•				W.				
	Application No.	Арр	licant(s)	<u> </u>				
	09/405,210	MAF	MARCHANT ET AL.					
Offic Action Summary	Examiner	Art	Unit					
•	DAVID VU	281	в					
The MAILING DATE of this communication app	ears on the cover s	heet with the corresp	ondence ad	dress				
Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.								
Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this commur     If the period for reply specified above is less than thirty (30) da be considered timely.     If NO period for reply is specified above, the maximum statutor communication.     Failure to reply within the set or extended period for reply will,  Status	nication. ys, a reply within the stary y period will apply and	atutory minimum of thirty will expire SIX (6) MONT	(30) days will HS from the m	ailing date of this				
1) Responsive to communication(s) filed on <u>03</u> .	April 2002 .							
,— .	nis action is non-fin	al.						
3) Since this application is in condition for allow closed in accordance with the practice under	ance except for for Ex parte Quayle,	mal matters, prosect 1935 C.D. 11, 453 C	ution as to t ).G. 213.	he merits is				
Disposition of Claims								
4) Claim(s) 1-17 is/are pending in the application	n.							
4a) Of the above claim(s) 4,5,9-11,16 and 17	is/are withdrawn fr	om consideration.						
5) Claim(s) is/are allowed.								
6)⊠ Claim(s) <u>1-3,6-8 and 12-15</u> is/are rejected.								
7) Claim(s) is/are objected to.								
8) Claims are subject to restriction and/o	or election requirem	ent.						
Application Papers								
9) The specification is objected to by the Examir	er.	·						
10) The drawing(s) filed on is/are objected to by the Examiner.								
11) The proposed drawing correction filed on	is:  a)∏ approv	ed b)∏ disapprove	ed.					
12) The oath or declaration is objected to by the E	Examiner.							
Priority under 35 U.S.C. § 119								
13) Acknowledgment is made of a claim for foreig	n priority under 35	U.S.C. § 119(a)-(d).						
a) All b) Some * c) None of the CERTIFIED copies of the priority documents have been:								
1. received.								
2. received in Application No. (Series Cod	de / Serial Number	·						
3. received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received.								
14) Acknowledgement is made of a claim for dom	estic priority under	35 U.S.C. & 119(e)	•					
Attachment(s)								
15) Notice of References Cited (PTO-892) 16) Notice of Draftsperson's Patent Drawing Review (PTO-948) 17) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	18)	Interview Summary (PT Notice of Informal Pater Other:						

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## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1. Claims 1-3, 6-8 and 12-15 are rejected under 35 U.S.C. 102(e) as being anticipated by Takahashi et al., (US 5,266,914).

Takahashi et al., in related text (Col. 4, Line. 57-Col. 6, Line. 5) and figures (Figs.3 and

4F), disclose a process for manufacturing a trench field effect transistor comprising the steps of:

etching a first trench 34 in a N-type substrate21;

lining the first trench 34 with a layer of dielectric material 25;

substantially filling the trench 34 with conductive material 26 to form a gate electrode of the field effect transistor;

implanting impurities of a second conductivity type (P-type) into the substrate21 to form a body region 23 having the second conductivity type (P-type) over the substrate21;

implanting impurities of the first conductivity type (N-type) inside the body region 23 to form a source region24 adjacent to the first trench34;

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etching a second trench 33 through the source region 24 and into the body region23, the second trench33 define by sidewalls extending into the body region23 and a bottom, which terminates below the source region24 and in contact with the body region23

and filling the second trench33 with conductive material 39 for making contact with both the source region 24 and the body region23.

## Conclusion

2. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Vu whose telephone number is (703) 305-0391. The examiner can normally be reached on Monday-Friday from 8:00am to 5:00pm. If attempt to

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reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms., can be reached on (703) 308-4910.

David Vu 🛚 🕽 🗸

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David Nelms
Supervisory Patent Examiner
Technology Center 2800